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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	10/612,837
Filing Date	
Inventor	
Assignee	
Group Art Unit	
Examiner	
Attorney Docket No	Ml22-2261
Title: Methods of Forming Circuit Trac	es and Contact Pads for Interposers
Utilized in Semiconductor Packages	·
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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

- 1. Certificate of Facsimile Transmission
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Dated: January 22, 2004	By:	Jamem White	
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FEE DEFICIENCY

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23,0925.

Dated: //22/04 By: David G. Latwesen, Ph.D.

David G. Latwesen, Ph.D. Reg. No. 38,533

Inventor:

Stephen F. Moxham

Title:

Methods of Forming Circuit Traces and Contact Pads for Interpos rs

Utilized in Semiconductor Packages

Assignee: Micron Technology, Inc.

INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 Off. Gaz. Pat. Off. 55, 05 August 2003). No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

David G. Latwesen, Ph.D.

Reg. No. 38,533

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